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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM FILING DATE CLASS SUBCLASS GAU EXAMINER
10067486 02/04/2002 257 655 2822 K. DUONG

**APPLICANTS: Tayanaka Hiroshi;

**CONTINUING DATA VERIFIED: Yes

THIS APPLICATION IS A CON OF 09/217,907 12/22/1998

KBD

BEST AVAILABLE COPY

** FOREIGN APPLICATIONS VERIFIED: Yes

JAPAN P09-360429 12/26/1997

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PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed

☒ yes ☐ no

35 USC 119 conditions met

☒ yes ☐ no

Verified and Acknowledged Examiners's initials

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ATTORNEY DOCKET NO

9792909-5361

TITLE : Semiconductor substrate and thin-film semiconductive member, and method for making thereof

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
ISSUE FEE		Sheets Drwg.	Figs. Drwg.
Amount Due	Date Paid	Print Fig.	
Primary Examiner		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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